

Purpose of the JST International Symposium on Dependable VLSI Systems 2012

I would like to welcome you to the First International Symposium on Dependable VLSI Systems. The purpose of this meeting is to have the results of a research program entitled, 'Dependable VLSI Systems,'

<http://www.dvlsi.jst.go.jp/english/index.html>

better known to those in the international community of engineering and management who take special interest in the dependability of systems being built and used in the real world.

This program, funded by JST (Japan Science and Technology Agency), was initiated in 2007 for the purpose of providing, from university-industry collaborations, basic VLSI technologies to enhance the dependability of VLSI systems, which could range from automobiles, plants, robots, transportation, telecom, data processing and aerospace. It consists of 11 projects that address various issues, some arising from miniaturization on the one hand and complexity on the other. The first 4 projects that started work in FY 2007 will be finishing their 5-year project terms in the current FY of 2012.

This year's symposium is structured as follows. First of all, we will have presentations by a few distinguished speakers on challenges in dependability and organized research activities on related topic around the world. We will then have principal investigators of the five DVLSI projects that will be finishing their terms in the current FY2012. That will be followed by posters and demos by all 11 project teams. Finally we will have a panel session to shed lights from the cross-hierarchical perspective.

I hope this meeting will provide an opportunity to more clearly identify key issues behind the dependability of VLSI systems, stimulate exchanging ideas, and help make progress toward innovative solution of those problems.

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